Appln. No.: 09/937,934

Amendment Dated: September 26, 2005 Reply to Office Action of: May 26, 2005

Remarks/Arguments:

Claims 1-12 and 20 are cancelled. Claims 13-19, 21-24 and 27-49 have been amended. Claims 50-52 have been added. No new material is introduced herein. Claims 13-19 and 21-52 are pending.

The title has been objected to as not being descriptive. A new title has been provided.

Claims 13-14 and 17-21 have been rejected under 35 U.S.C. §102(e) as being anticipated by Yanagihara (U.S. Pat. No. 6,115,531). Claim 20 has been cancelled. It is respectfully submitted, however, that the remaining claims are now patentable over the cited art for the reasons set forth below.

Claim 13, as amended, includes features neither disclosed nor suggested by the cited art, namely:

...<u>time intervals generating means</u> which generates predetermined time interval information...

...outputting means which outputs <u>a series of data frames as stream data</u> <u>based on said generated predetermined time interval information</u>...

...<u>buffering means</u> which <u>stores said output stream data and said generated predetermined time interval information</u>...

...sending means which <u>divides said stored stream data into packet data</u> and sends said packet data...

...transmission managing means which <u>enters said stored stream data to said</u> <u>sending means based on said stored predetermined time interval</u> information...

These features are disclosed, for example, p. 41, line 11-p. 44, line 7, and Figure 6.

Yanagihara discloses in Fig. 4 a digital VTR that includes <u>a switching circuit</u> 4 for switching between <u>a transport packet of an MPEG2 signal</u> and <u>a video signal</u> such as from an

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NTSC system. When the <u>transport packet of the MPEG2 signal is recorded</u>, it is <u>inputted to frame forming circuit 5 through a rate conversion and format converting unit 9</u> (Col. 7, line 66-Col. 8, line 3). When the <u>video signal is recorded</u>, it is converted to a digital signal and <u>compressed</u> by DCT compressing circuit 3 and <u>supplied to frame forming circuit 5</u> (Col. 8, lines 6-10). An output of the frame forming circuit is supplied to a channel coder 6. The output of channel coder 6 is recorded to tape (Col. 7, lines 47-54)

Yanagihara discloses that for <u>recording with a transport packet of an MPEG2 signal</u>, the transport packet is applied to <u>a rate conversion and format converting unit</u> 9. The rate conversion and format converting unit <u>selects a program from the transport packet</u> and <u>converts the rate</u> of the selected program to a predetermined rate for three modes (Col. 7, lines 41-44). The rate conversion and format converting buffer <u>adds time base information</u> to <u>return data to the same time base during reproduction</u> as existed before recording (Col. 8, lines 19-22). The added time base information is <u>used for a transport packet during reproduction</u> (Col. 14, lines 40-52).

Yanagihara does not disclose or suggest Applicants claimed features of "...outputs a series of data frames as stream data based on ... predetermined time interval information...", "buffering means which stores said output stream data and ... predetermined time interval information...", "...sending means which divides said stored stream data into packet data and sends said packet data..." and "...transmission managing means which enters said stored stream data to said sending means based on said stored predetermined time interval information..." (emphasis added). These features are neither disclosed nor suggested by Yanagihara. Thus, Yanagihara does not include all of the features of amended claim 13. Accordingly, allowance of claim 13 is respectfully requested.

Claims 14, 17-19 and 21 include all of the features of claim 13 from which they depend. Accordingly, claims 14, 17-19 and 21 are also patentable over the cited art.

Claims 15-16 and 22-49 have been rejected under 35 U.S.C. §103(a) as being unpatentable over Yanagihara. Claims 15 and 16, however, also include all of the features of claim 13 from which they depend. It is respectfully submitted, however, that the remaining claims are now patentable over the cited art for the reasons set forth below.

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Claim 22, as amended, includes features neither disclosed nor suggested by the cited art, namely:

...data conversion means comprising...

...packet generating means which <u>divides input stream data and adds header</u> information to each piece to produce packets...

...packet processing start time inserting means which inserts calculated packet processing start time information into the header information of at least a first packet of each frame of said stream data, said data conversion means outputting the packets produced by said data conversion means...

...<u>an interface comprising</u>...<u>transmission start time controlling means</u> which controls transmission start time <u>based on said packet processing start time</u> information...

...said interface <u>outputting to a bus the packets processed</u> by said data conversion means at said transmission start time... (Emphasis added)

These features are disclosed, for example, p. 48, line 22-p.55, line 8; and Figure 8.

Yanagihara is described above. Yanagihara does not disclose or suggest Applicants claimed features of "...packet generating means which <u>divides input stream data</u> and adds header information to each piece <u>to produce packets</u>... <u>packet processing start time inserting means</u> which <u>inserts calculated packet processing start time information</u> into the header information of at least a first packet of each frame... <u>transmission start time controlling means</u> which controls transmission start time <u>based on said packet processing start time information</u>... said interface <u>outputting to a bus the packets processed</u> by said data conversion means <u>at said transmission start time</u>..." (emphasis added). These features are neither disclosed nor suggested by Yanagihara. Thus, Yanagihara does not include all of the features of amended claim 22. Accordingly, allowance of claim 22 is respectfully requested.

Claims 23 and 24 include all of the features of claim 22 from which they depend. Claims 25 and 26 include all of the features of claims 13 or 22 from which they depend. Accordingly, claims 23-26 are also patentable over the cited art.

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The rejection of claim 27 is respectfully traversed. Claim 27 includes features neither disclosed or suggested by Yanagihara. Namely:

...an interface which <u>receives a transmission packet</u> which contains a transmission path header <u>in which additional information is described and data blocks</u>...

...transmission path header separator means which separates said transmission packet into said transmission path header and said data blocks...

...additional information extracting means which <u>extracts said additional</u> information from said transmission path header...

...data packet generating means which generates from said data blocks a data packet which is obtained by combining one or more of said data blocks...

...additional information inserting means which adds or inserts said additional information to said data packet and outputs as an output packet the data packet together with said additional information of a type of data format which can be processed by an application simultaneously... (Emphasis added)

Yanagihara discloses that during recording of the transport packet of an MPEG2 signal, one byte of the sync data is eliminated and time information of three bytes is added (Co. 8, lines 30-34). During reproduction, Fig. 22, the three bytes of time base information added to the header are detected and the proper time base is determined for reproduction (Col. 14, lines 44-47). Yanagihara does not disclose or suggest Applicants claimed features of "...an interface which receives a transmission packet... in which additional information is described and data blocks... data packet generating means which generates from said data blocks a data packet... ...additional information inserting means which adds or inserts said additional information to said data packet and outputs as an output packet the data packet together with said additional information... which can be processed by an application simultaneously..." These features are neither disclosed or suggested by Yanagihara. Thus, Yanagihara does not include all of the features of claim 27. Accordingly, allowance of claim 27 is respectfully requested.

Claims 28-30 include all of the features of claim 27 from which they depend. Accordingly, claims 28-30 are also patentable over the cited art.

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The rejection of claim 31 is respectfully traversed. Although not identical, claim 31 includes features similar to claim 27 which are not disclosed or suggested by the cited art. Accordingly, allowance of claim 31 is respectfully requested.

Claims 32-34 include all of the features of claim 31 from which they depend. Claims 35-37 include all of the features of claims 27 or 31 from which they depend. Accordingly, claims 32-37 are also patentable over the cited art.

The rejection of claim 38 is respectfully traversed. Although not identical, claim 38 includes features similar to claim 27 which are not disclosed or suggested by the cited art. Accordingly, allowance of claim 38 is respectfully requested.

Claims 39-41 include all of the features of claim 38 from which they depend. Accordingly, claims 39-41 are also patentable over the cited art.

The rejection of claim 42 is respectfully traversed. Although not identical, claim 42 includes features similar to claim 27 which are not disclosed or suggested by the cited art. Accordingly, allowance of claim 42 is respectfully requested.

Claims 43 and 44 include all of the features of claim 42 from which they depend. Claims 45-47 include all of the features of claims 38 or 42 from which they depend. Claims 48 and 49 include all of the features of claims 27, 31, 38 or 42 from which they depend. Accordingly, claims 43-49 are also patentable over the cited art.

Claims 50-52 are newly added. These claims do not add new matter. Support for these claims can be found in the specification, for example, p. 59, lines 10-19 for claim 50; p. 65, lines 1-10 for claim 51; and p. 66, line 6 - p. 67, line 10 for claim 52.

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In view of the amendments and arguments set forth above, the above-identified application is in condition for allowance which action is respectfully requested.

Respectfully submitted,

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DNC/ds/fp/ds

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September 26, 2005

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